

### REMARKS

This application is a continued prosecution application of U.S. Patent Application Serial Number 09/981,960, filed on October 18, 2001.

The Examiner rejected claims 1-3, 9, 12, 19, 20, 23, 30, 40, and 41 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of U.S. Publication 2002/0053001 to Miura *et al.*

The Examiner rejected claims 4 – 7 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of U.S. Publication 2002/0053001 to Miura *et al.*, and further in view of U.S. Publication 2002/0118175 to Liebenow *et al.* The Examiner rejected claim 8 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of U.S. Publication 2002/0053001 to Miura *et al.*, and further in view of U.S. Publication 2002/0101907 to Dent *et al.* The Examiner rejected claims 10, 11, 13 – 18, 21, 22, 24 – 29, and 31 - 34 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of U.S. Publication 2002/0053001 to Miura *et al.*, and further in view of Applicants' admitted prior art. The Examiner rejected claims 44 - 47 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of Applicants' admitted prior art. The Examiner rejected claims 48 - 51 under 35 U.S.C. 103(a) as being unpatentable over U.S. Publication 2003/0036351 to Forbs in view of U.S. Publication 2002/0053001 to Miura *et al.*, and further in view of U.S. Patent No. 4,870,704 to Matelan *et al.*

Applicants have amended independent claims 1, 2, 19, 30, and 40.


Each independent claim (1, 2, 19, 30, and 40) now requires that a memory module include electrical terminals for sending data to and receiving data from a second circuit board. Thus, all independent claims now require that the memory module (1) be operable to communicate with the second circuit board via radio transmissions and (2) be operable to communicate with the same circuit board via a plurality of electrical terminals on the memory module. It is Applicants' understanding that none of the cited prior art teaches a memory module that can communicate with a second circuit board both (1) via radio transmissions and (2) via a plurality of terminals. Thus, Applicants believe that all currently pending claims are allowable over the art of record.

**CONCLUSION**

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

By

  
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